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AMENDMENTS TO CLAIMS

1. (Currently amended) A data storage device comprising:
a conductive probe having a tip;
a substrate including a semiconductor portion; and
a data storage medium including a layer of poled ferroelectric material for storing data, the ferroelectric layer on the substrate, between the tip and the substrate, the semiconductor portion and the ferroelectric layer forming an electrical junction.
2. (Original) The device of claim 1, wherein the ferroelectric material includes a ferroelectric polymer.
3. (Original) The device of claim 1, wherein the ferroelectric material includes at least one of PVDF and PtrFE.
4. (Original) The device of claim 1, wherein the ferroelectric material includes an inorganic material.
5. (Original) The device of claim 1, wherein the probe tip is no more than about 100 nanometers in diameter.
6. (Original) The device of claim 1, wherein electrical dipoles in the ferroelectric layer are oriented in a reference direction; the device further comprising a circuit for causing the probe to write to spaced-apart volumes on the ferroelectric layer, dipoles in the spaced apart volumes oriented in the reference direction to store a first logic value, and a different direction to store a second logic value.

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7. (Original) The device of claim 1, further comprising a protective layer covering the ferroelectric layer, the protective layer not interfering with interactions between the probe tip and the ferroelectric layer.
8. (Original) The device of claim 1, further comprising a circuit for causing the conductive probe to apply local electric fields to the ferroelectric layer during write operations, the local electric fields causing local polarization changes in the ferroelectric material.
9. (Original) The device of claim 1, further comprising a circuit for causing the conductive probe to perform block and bulk erasure operations.
10. (Original) The device of claim 1, further comprising means for heating the ferroelectric material above its Curie temperature, whereby block and bulk erasure can be performed.
11. (Currently amended) ~~The device of claim 1, wherein the substrate includes a semiconductor portion, the semiconductor portion and the ferroelectric layer forming an electrical junction, the device further~~ comprising a read circuit for using the probe to sense changes in capacitance or leakage current of the junction.
12. (Currently amended) ~~The device of claim 1, wherein the substrate includes a semiconductor portion, the semiconductor portion and the ferroelectric layer forming an electrical junction, the device further~~ comprising a read circuit for using the probe to apply an ac signal to

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local areas on the ferroelectric material, and detect changes in a non-linear component of a dielectric constant.

13. (Withdrawn-currently amended) The device of claim 1, wherein the ~~substrate includes a semiconductor portion, the semiconductor portion and the ferroelectric layer forming a junction, there being further~~ comprising a conductive channel in the vicinity of the junction, width of the conductive channel spanning multiple bits, the device further comprising a circuit for reading the bits, the circuit sensing at least one property of the channel.
14. (Cancelled)
15. (Cancelled)
16. (Cancelled)
17. (Cancelled)
18. (Cancelled)
19. (Cancelled)
20. (Cancelled)
21. (Currently amended) A data storage device comprising:
a semiconductor substrate;

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a data storage medium including a layer of poled ferroelectric material on the semiconductor substrate, the ferroelectric material and the substrate forming an electrical junction;

a plurality of sharp-tip probes;

means for causing the probes to create local polarization changes in the ferroelectric layer during write operations; and

means for using the probes to detect polarization of local areas on the ferroelectric layer during read operations.

22. (Original) A method of writing information to a layer of poled ferroelectric material, the method comprising using a probe to create local polarization changes in the material, the probe having a tip diameter no more than several nanometers.
23. (Original) The method of claim 22, wherein electrical dipoles in the ferroelectric layer are oriented in a reference direction; wherein the probe is used to write to spaced-apart volumes on the ferroelectric layer, dipoles in the spaced apart volumes oriented in the reference direction to store a first logic value, and a different logic direction to store a second logic value.
24. (Original) The method of claim 22, wherein the probe is used to write to spaced apart locations on the ferroelectric layer such that the spaced apart locations have the same electrical dipole alignment as the rest of the ferroelectric layer, whereby information at the spaced apart locations is erased.

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25. (Original) The method of claim 22, further comprising heating the ferroelectric layer above its Curie temperature, whereby block erasure of the ferroelectric layer is performed.

26. (Original) The method of claim 22, further comprising heating selected areas of the ferroelectric layer above the Curie temperature of the ferroelectric layer, whereby the areas of the ferroelectric layer are erased.

27. (Currently amended) A method of reading information from a ferroelectric layer that is on a semiconductor substrate, and forms an electrical junction with the semiconductor substrate, the method comprising:

scanning a surface of the ferroelectric layer with a probe having a sharp tip, the tip having a diameter of several nanometers; and

using the probe and the semiconductor substrate to detect polarity reversals at designated locations on the ferroelectric layer, each polarity reversal at a designated location indicating a first stored value at that designated location, each non-reversal of polarity at an expected location indicating a second logic value stored at that designated location.

28. (Currently amended) The method of claim 27, wherein the ~~ferroelectric layer is on a semiconductor substrate, the substrate and the ferroelectric layer forming an electrical junction~~, the probe [[being]] is used to sense changes in capacitance or leakage current of the junction.

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29. (Currently amended) The method of claim 27, ~~wherein the ferroelectric layer is on a semiconductor substrate, the substrate and the ferroelectric layer forming an electrical junction,~~ wherein the probe is used to apply an ac signal to local areas on the ferroelectric material, and wherein changes in a non-linear component of a dielectric constant are detected.
30. (Withdrawn – currently amended) The method of claim 27, wherein the ~~ferroelectric layer is on a semiconductor substrate, the substrate and the ferroelectric layer forming an electrical junction, there being a~~ conductive channel is in the vicinity of the junction, the conductive channel spanning multiple bits, wherein reading the bits includes sensing at least one property of the channel, wherein the probe is used to flip dipoles spanned by the channel.
31. (Cancelled)
32. (Cancelled)
33. (Cancelled)
34. (Cancelled)
35. (Cancelled)
36. (Withdrawn) A method of fabricating a data storage medium, the method comprising forming a film on a substrate by coating a dissolved ferroelectric polymer on the substrate and heating to remove solvent in

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the polymer; and the combination of sputtering the polymer from a solid source and applying a Langmuir-Blodgett process repeatedly.

37. (Withdrawn) The method of claim 36, further comprising polarizing the film.